

## United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.	
09/824,701	04/04/2001	Robert Alan Williams	F0700	F0700 9657	
45114	7590 04/01/2005		EXAM	INER	
HARRITY & SNYDER, LLP 11240 WAPLES MILL ROAD SUITE 300			MATTIS, JASON E		
			ART UNIT	PAPER NUMBER	
FAIRFAX, V	A 22030		2665		

DATE MAILED: 04/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	( <b>%</b>		
	Application No.	Applicant(s)	
	09/824,701	WILLIAMS, ROBERT ALAN	
Office Action Summary	Examiner	Art Unit	
	Jason E Mattis	2665	
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the (	correspondence address	
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION.  - Extensions of time may be available under the provisions of 37 CFR 1.1: after SIX (6) MONTHS from the mailing date of this communication.  - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period v - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be ting within the statutory minimum of thirty (30) day will apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	mely filed ys will be considered timely. In the mailing date of this communication. ED (35 U.S.C. § 133).	
Status			
Responsive to communication(s) filed on      This action is <b>FINAL</b> . 2b)⊠ This      Since this application is in condition for allowar closed in accordance with the practice under E	action is non-final. nce except for formal matters, pro		
Disposition of Claims			
4) ☐ Claim(s) 1-20 is/are pending in the application. 4a) Of the above claim(s) is/are withdray 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-20 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	vn from-consideration.		
Application Papers			
9) The specification is objected to by the Examine 10) The drawing(s) filed on is/are: a) acce Applicant may not request that any objection to the Replacement drawing sheet(s) including the correct 11) The oath or declaration is objected to by the Ex	epted or b) objected to by the drawing(s) be held in abeyance. Se ion is required if the drawing(s) is ob	e 37 CFR 1.85(a). ejected to. See 37 CFR 1.121(d).	
Priority under 35 U.S.C. § 119			
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of:  1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the prior application from the International Bureau * See the attached detailed Office action for a list	s have been received. s have been received in Applicat rity documents have been receive u (PCT Rule 17.2(a)).	ion No ed in this National Stage	
Attachment(s)  1) Notice of References Cited (PTO-892)  2) Notice of Draftsperson's Patent Drawing Review (PTO-948)  3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)  Paper No(s)/Mail Date	4) Interview Summary Paper No(s)/Mail D 5) Notice of Informal F 6) Other:		

Art Unit: 2665

## **DETAILED ACTION**

## Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
  - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-20 are rejected under 35 U.S.C. 103(a) as being unpatentable over Sampath et al. (U.S. Publication US 2002/0009081 A1) in view of "SmartSwitch 2000 Firmware version 4.05.06".

With respect to claim 1, Sampath et al. discloses a network device configured to control communication of data frames between stations (See page 3 paragraph 61 and Figure 1 of Sampath et al. for reference to switch-on-chip (SOC) 10, which is a network device controlling communication of data frames between stations). Sampath et al. also discloses a plurality of receive ports configured to receive data frames from the stations (See page 3 paragraph 62 and Figure 1 of Sampath et al. for reference to ports 31). Sampath et al. further discloses a memory configured to store address information and data forwarding information associated with the received data frames (See Figure 1 of Sampath et al. for reference to ARL table 31, which is a memory storing address and data forwarding information). Sampath et al. does

Art Unit: 2665

not specifically disclose processing and forwarding frames to destination addresses without modifying the frames when operating in accordance with a first protocol and processing and forwarding frames to destination addresses with at least one of the frames being modified before being forwarded when operating in accordance with a second protocol.

With respect to claim 8, Sampath et al. discloses a method in a network device that controls communication of data frames between stations (See page 3 paragraph 61 and Figure 1 of Sampath et al. for reference to switch-on-chip (SOC) 10, which is a network device controlling communication of data frames between stations). Sampath et al. also discloses storing information including address information and data forwarding information in a memory of a network device (See Figure 1 of Sampath et al. for reference to ARL table 31, which is a memory storing address and data forwarding information). Sampath et al. further discloses receiving data frames on a plurality of receive ports of the network device (See page 3 paragraph 62 and Figure 1 of Sampath et al. for reference to ports 31, which data frames are received on). Sampath et al. does not specifically disclose setting an operating mode to at least one of a first operating mode and a second operating mode. Sampath et al. also does not specifically disclose processing and forwarding frames to destination addresses without modifying the frames when operating in accordance with a first protocol and processing and forwarding frames to destination addresses with at least one of the frames being modified before being forwarded when operating in accordance with a second protocol.

Art Unit: 2665

With respect to claim 15, Sampath et al. discloses a network device (See page 3 paragraph 61 and Figure 1 of Sampath et al. for reference to switch-on-chip (SOC) 10, which is a network device). Sampath et al. also discloses a plurality of received and transmit ports configured to received and transmit data frames (See page 3 paragraph 62 and Figure 1 of Sampath et al. for reference to ports 31, which are a plurality of receive and transmit ports). Sampath et al. further discloses a memory configured to store address information and data forwarding information associated with the received data frames (See Figure 1 of Sampath et al. for reference to ARL table 31, which is a memory storing address and data forwarding information). Sampath et al. also discloses a decision making engine configured to identify data forwarding information identifying at least a first one of the transmit ports and a first virtual local area network for a first frame (See page 5 paragraph 94 and page 7 paragraphs 126-139 of Sampath et al. for reference to an ARL Engine and a Fast Filtering Process (FFP), which together are a decision making engine, searching an ARL table to identify data forwarding information including a TGID/Port Number, which identifies a transmit port, and VID---VLAN ID, which identifies a virtual local area network). Sampath et al. further discloses generating a forwarding descriptor for the first data frame including an untagged set field identifying at least one transmit port, and a first opcode field including information identifying whether the first data frame was at least one of untagged, VLAN-tagged, and priority-tagged (See page 5 paragraph 95 and page 7 paragraph s126-139 of Sampath et al. for reference to the ARL engine outputting a result the ARL search and the FFP output, which

Art Unit: 2665

transmit ports, and also including information about whether the frame was untagged, VLAN-tagged, or priority tagged). Sampath et al. does not specifically disclose generating an opcode indicating that the frame is to be transmitted without modification when operating in accordance with a first protocol and generating a second opcode indicating that the frame is to be transmitted without a VLAN tag, with a VLAN tag, or without modification when the network is operating in accordance with a second protocol and based on the contents of the untagged set field and the first opcode.

With respect to claims 2, 9, and 16, Sampath et al. does not specifically disclose that the first protocol is IEEE 802.1D and the second protocol is IEEE 802.1Q.

With respect to claims 1-2, 8-9 and 15-16, the *SmartSwitch* paper, in the field of communications, disclose a switch that is programmable to operate in accordance with a first protocol, 802.1D, or in accordance with a second protocol, 802.1Q (See page 3 paragraph 2 of the *SmartSwitch* paper for reference to selecting the operational mode as either 802.1D or 802.1Q). Since, as disclosed in the Applicant's own Background Art section, when operating in accordance with 802.1D data frames must be forwarded exactly the way they were received, and when operating in accordance with 802.1Q, it is sometimes necessary to modify data frames before forwarding, the switch disclosed in the *SmartSwitch* paper must also follow these rules. Using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol has the advantage of creating more flexibility by being able to

Art Unit: 2665

use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

It would have been obvious for one of ordinary skill in the art at the time of the invention, when presented with the work of the *SmartSwitch* paper, to combine using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol, as suggested by the *SmartSwitch* paper, with the network device and method of Sampath et al., with the motivation being to create more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

With respect to claims 3 and 10, Sampath et al. does not specifically disclose a register configured to store information indicating whether the network device is operating in accordance with a first protocol, reading the contents of the register, and determining whether the network device is operating in accordance with the first IEEE 802.1D protocol of the second IEEE 802.1Q protocol.

With respect to claims 3 and 10, the *SmartSwitch* paper, in the field of communications, disclose a switch that is programmable to operate in accordance with a first protocol, 802.1D, or in accordance with a second protocol, 802.1Q (See page 3 paragraph 2 of the *SmartSwitch* paper for reference to selecting the operational mode as either 802.1D or 802.1Q). Since, the switch disclosed in the *SmartSwitch* paper can operate in either 802.1D protocol or 802.1Q, it must contain a register storing information indicating the current operating mode of the switch that is used to determine whether it is operating in accordance with 802.1D protocol or in accordance with 802.1Q

Art Unit: 2665

protocol. Using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol has the advantage of creating more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

It would have been obvious for one of ordinary skill in the art at the time of the invention, when presented with the work of the *SmartSwitch* paper, to combine using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol, as suggested by the *SmartSwitch* paper, with the network device and method of Sampath et al., with the motivation being to create more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

With respect to claims 4-5, 11-12, and 17-18, Sampath et al. discloses retrieving a data frame received on one of the receive ports and transmitting the received data frame to the first transmit port (See page 5 paragraphs 94-97 of Sampath et al. for reference to determining the egress ports of a received packet and transmitting the packet to the egress ports). Sampath et al. does not specifically disclose that when operating in accordance with the first protocol, forwarding the data frame to the port identified by forwarding information without at least one of inserting virtual local area network information into the frame, deleting VLAN information included with the frame, and modifying VLAN information included with the frame. Sampath et al. also does not specifically disclose that when operating in accordance with the second protocol, doing at least one of inserting VLAN information into the

Art Unit: 2665

received data frame, deleting VLAN information included with the received data frame, and modifying the VLAN information included with the received data frame based on whether the first transmit port is a member of an untagged set for the first VLAN.

With respect to claims 4-5, 11-12, and 17-18, the *SmartSwitch* paper, in the field of communications, disclose a switch that is programmable to operate in accordance with a first protocol, 802.1D, or in accordance with a second protocol, 802.1Q (See page 3 paragraph 2 of the *SmartSwitch* paper for reference to selecting the operational mode as either 802.1D or 802.1Q). Since, as disclosed in the Applicant's own Background Art section, when operating in accordance with 802.1D data frames must be forwarded exactly the way they were received, and when operating in accordance with 802.1Q, it is sometimes necessary to modify data frames, by inserting, deleting, or modifying VLAN information included in a frame before forwarding, the switch disclosed in the *SmartSwitch* paper must also follow these rules. Using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol has the advantage of creating more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

It would have been obvious for one of ordinary skill in the art at the time of the invention, when presented with the work of the *SmartSwitch* paper, to combine using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol, as suggested by the *SmartSwitch* paper, with the network device and method of Sampath et al., with the motivation being to create more flexibility by being

Art Unit: 2665

able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

With respect to claims 6 and 13, Sampath et al. discloses identifying forwarding information for a first one of the received data frames (See page 5 paragraph 94 and page 7 paragraphs 126-139 of Sampath et al. for reference to an ARL Engine and a Fast Filtering Process (FFP), searching an ARL table to identify data forwarding information). Sampath et al. also discloses generating a forwarding descriptor for the first data frame including an untagged set field identifying at least one transmit port, and a first opcode field including information identifying whether the first data frame was at least one of untagged, VLAN-tagged, and priority-tagged (See page 5 paragraph 95 and page 7 paragraph s126-139 of Sampath et al. for reference to the ARL engine outputting a result the ARL search and the FFP output, which together a forwarding descriptor, including the egress port/ports, which is/are transmit ports, and also including information about whether the frame was untagged, VLAN-tagged, or priority tagged).

With respect to claims 7 and 14, Sampath et al. does not specifically disclose deleting a VLAN tag in the first data frame based on the contents of the untagged set field and the opcode field and whether the device is operating in accordance with the second protocol.

With respect to claims 7 and 14, the *SmartSwitch* paper, in the field of communications, disclose a switch that is programmable to operate in accordance with a first protocol, 802.1D, or in accordance with a second protocol, 802.1Q (See page 3)

Art Unit: 2665

paragraph 2 of the *SmartSwitch* paper for reference to selecting the operational mode as either 802.1D or 802.1Q). Since, as disclosed in the Applicant's own Background Art section, when operating in accordance with 802.1Q, it is sometimes necessary to delete a VLAN tag included in a frame before forwarding, the switch disclosed in the *SmartSwitch* paper must also follow these rules. Using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol has the advantage of creating more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

It would have been obvious for one of ordinary skill in the art at the time of the invention, when presented with the work of the *SmartSwitch* paper, to combine using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol, as suggested by the *SmartSwitch* paper, with the network device and method of Sampath et al., with the motivation being to create more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

With respect to claim 19, Sampath et al. does not specifically disclose assigning a default VLAN index to received data frames when the network device is operating in accordance with the first protocol.

With respect to claim 19, the *SmartSwitch* paper, in the field of communications, disclose a switch that is programmable to operate in accordance with a first protocol, 802.1D, or in accordance with a second protocol, 802.1Q (See page 3)

Art Unit: 2665

paragraph 2 of the *SmartSwitch* paper for reference to selecting the operational mode as either 802.1D or 802.1Q). Since, as disclosed in the Applicant's own Background Art section, when operating in accordance with 802.1D data frames are assigned a default VLAN index, the switch disclosed in the *SmartSwitch* paper must also follow these rules. Using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol has the advantage of creating more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

It would have been obvious for one of ordinary skill in the art at the time of the invention, when presented with the work of the *SmartSwitch* paper, to combine using a switch that is programmable to operate in either a first 802.1D protocol or a second 802.1Q protocol, as suggested by the *SmartSwitch* paper, with the network device and method of Sampath et al., with the motivation being to create more flexibility by being able to use the switch in multiple network environments, specifically either a network using the 802.1D protocol or a network using the 802.1Q.

With respect to claim 20, Sampath et al. discloses that the decision making device is configured to at least one of assign a VLAN index to a received data frame based on the port on which the data frame was received and assign a VLAN index to a received data frame based on a VLAN identifier included in the received frame, when the network device is operating in accordance with the second protocol (See page 7 paragraph 127 of Sampath et al. for reference to assigning a VID, which is a VLAN index, based on port number if the packet was untagged, and for reference to

Page 12

Application/Control Number: 09/824,701

Art Unit: 2665

assigning a VID based on the VID present in the VLAN tag if the packet was

tagged).

Conclusion

Any inquiry concerning this communication or earlier communications from the

examiner should be directed to Jason E Mattis whose telephone number is (571) 272-

3154. The examiner can normally be reached on M-F 8AM-4:30PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's

supervisor, Huy Vu can be reached on (571) 272-3155. The fax phone number for the

organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the

Patent Application Information Retrieval (PAIR) system. Status information for

published applications may be obtained from either Private PAIR or Public PAIR.

Status information for unpublished applications is available through Private PAIR only.

For more information about the PAIR system, see http://pair-direct.uspto.gov. Should

you have questions on access to the Private PAIR system, contact the Electronic

Business Center (EBC) at 866-217-9197 (toll-free).

jem

HUY D. VU

SUPERVISORY PATENT EXAMINER

TECHNOLOGY CENTER 2600